**ENGINEERING EXAMINATION SYSTEM** 

COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

RC 2016-17

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EXAM YEAR: November 2019

Bachelor of Engineering (Electronics & Telecommunication)
College :-Government of Goa College of Engineering

SEAT NO	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM- V		NAME OF THE CANDIDATE MA	X . 100	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 850 340	REMARK
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	Digital Signa	al Processin	ng				75	15	18	108P				
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	Control Sys	tems Engine	eering				52	20	<ul> <li>edit serviror il sociologico con estado coloridad de contrato coloridad de contrato de co</li></ul>	72P				
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	VLSI Design		County Cheprocure Languages			rate mayor a community of decomplants. A resolving also a processors a polytoper a processor and a consequent	82	23		105P	25P	ner de de merk de militar vinsker (dies vinskerverver er		
	Analog Con	nmunication				nganisation for the control of the boundary of country and the first of the control of the country of the country of	58	19		77P	an participation of the system of the state	22P		and the state of t
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190659	201704607	1	Α		F	BALE SIMREN	PRADEEP							
	Digital Sign	al Processin	ng			playing a stranger out management and stranger that the foreign that side through recent states are str	28	16	19	63F	ellegt gener der en gener ellegt de terrelagiest, delegante normal			
	Transmission			nnas			40	11		51P				
	Control Sys				ma, at laterary, and the work	performing of the specific of the sector between the sector between the contract of the sector of the sector between the sector of the sector	44	11		55P		and the section of the colours of the section of places of the section of		
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190660	201705015	1	Α		F	BARAD DIKSHA	DAMODA	R						
	Digital Sign	al Processin	ng				55	8	10	73P				
	Transmission	on Lines and	d Ante	nnas			40	17		57P				
	Control Sys	tems Engine	eering				47	12		59P				
	Embedded	Systems					72	16		88P	11P			es promotion de la deservación de la composition della composition
	VLSI Desig	n and Techr	nology				58	17	the residence of the control of the publication of the control of	75P	15P			
	Analog Con	nmunication					51	14		65P	es afficia de terra como esta esta esta esta esta esta esta esta	10P		
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190661	201704606	1	Α	The register of register of the part of	М	BHONSLE SAHIL	JAIPRASS	SAD	et yn draeth yn selwyd yn terminyd a cyflon a rynnyn of drog en y dy y	and many for the contraction of	na nigatina nahi di anun araban anahabi kana bara kawasan, antaka 	y hiji manani yani ki ang higilanan inaya ani ang ki sanang	t yet olliget god benavis, in a region older get a ever, yeth etail.	yer plants to
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	VLSI Desig	the growth facilities of the control	-				64	14		78P	22P	And other designs on the second or second or second ones		
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190662	201704587	1	Α	*	M	BURYE CHIRAG	NITIN				The participant of a control of the	mer updament i o'yonda ah iyonda ah iyo hay ayondayadan san d	y Milanday ( y ) y h. a. c. y Assistance ( y magain y high clines	
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	Embedded		State and the state of the stat	ti ngantingani ng mpantani na asah	decinique aplicativa <sub>e</sub> - <b>q</b> ipaghashish <sub>e</sub> s	эт энгэл эх нь нэг хүйл хүмгэг баргаан хүмгэг хүргэг хүрэг хүр эх хүргэг хүргэг хүргэг хүргэг хүргэг хүргэг хүр	88	18		106P	24P		ingle or some over the some of the some of the source of t	
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	Embedded	Systems					44	13		57P	17P			
	VLSI Design	n and Techn	nology				49	15		64P	17P			
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### **ENGINEERING EXAMINATION SYSTEM**

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COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Bachelor of Engineering (Electronics & Telecommunication)

SEAT NO	P.R. NO.	ATTEMPTS	ARE	PRO	SEX	NAME OF THE	1	THEORY	SESSIONAL	TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	
DEAT NO	1 .K. 110.	ATTEMITY	CD	SEM-		CANDIDATE	MAX . MIN .		25	25	125 OR 150 50 OR 60	50 OR 25 25 OR 10	50 OR 25 25 OR 10	850	REMARK
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yer birdir dir fi Virginis allih brasidi ya samindi i penis dia pinis di	Digital Signa	en president proportiere and and out-object programmer by the times of a	erabition you is allow to being individual or the	inghidestinaheterio Antalias visas		n din hy ne gydda ggwyd y yggwraithoga gwrai e e er man he dywrefol efellogiol fe	Aprilia finale d'altre d'altre des describes de l'Arres des de l'Arres des describes de l'Arres des de l'Arres	66	13	18	97P		es equicados estados estados estados entre estados entre estados entre estados entre estados entre entre estad	and a second effectively. Freeze, in the second of the second of	
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190669	201704605	1	Α		F	DHAWADE 1	NAIK M		en <sup>k</sup> anderfagtingen stjeren på andra en	<b>VT</b> 	and frequency of an antiperior and a second state of the second				
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THE LITE STREET TO THE REAL PROPERTY OF THE PR		to grad a substitution and accompany	anticopper property of the contract	Not the control of the control	range have been seen	Progression and American Statements	1		Average course					556	Passes
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COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Bachelor of Engineering (Electronics & Telecommunication) RC 2016-17 College :-Government of Goa College of Engineering **EXAM YEAR:**November 2019 ATTEMPTS ARE SEAT NO P.R. NO. PRO SEX NAME OF THE THEORY SESSIONAL TERM WORK TOTAL PRACTICAL TOTAL ORAL CANDIDATE 125 OR 150 50 OR 25 850 SEM-50 OR 25 REMARKS 100 50 OR 60 25 OR 10 25 OR 10 190671 201705094 DONGREKAR TANVI VINOD 1 20 97P Digital Signal Processing 14 51 18 69P Transmission Lines and Antennas 54 21 75P Control Systems Engineering 57 14 71P 25P **Embedded Systems** VLSI Design and Technology 90 19 109P 24P **Analog Communication** 56 19 75P 21P 566 Passes 190672 201705123 FERNANDES CRIZEL MUREL PEDRINA 82 16 119P Digital Signal Processing 64 24 88P Transmission Lines and Antennas 61 23 84P Control Systems Engineering **Embedded Systems** 96 20 116P 23P 95 24P VLSI Design and Technology 23 118P 74 21 95P **Analog Communication** 24P 691 Passes 190673 201704582 FERNANDES AARON LEEVORD Digital Signal Processing 10 15 82P Transmission Lines and Antennas 40 10 50P 42 13 Control Systems Engineering 55P 47 **Embedded Systems** 11 58P 22P 67 13 VLSI Design and Technology 80P 24P **Analog Communication** 48 8 56P 16P 443 Passes 190674 201704592 FERNANDES ELAINE MICHELLE Digital Signal Processing 67 12 18 97P Transmission Lines and Antennas 76 22 98P 68 19 Control Systems Engineering 87P 74 **Embedded Systems** 16 90P 22P 87 VLSI Design and Technology 24 111P 25P **Analog Communication** 62 17 79P 24P 633 Passes 190675 201704608 1 GADKAR SIDDHARTH VISHWANATH Digital Signal Processing 13 15 96P Transmission Lines and Antennas 46 15 61P 49 Control Systems Engineering 19 68P **Embedded Systems** 73 13 86P 21P VLSI Design and Technology 75 17 92P 25P **Analog Communication** 52 12 64P 19P 532 Passes 190676 201704568 GAUNEKAR SAISH BABULI Digital Signal Processing 37\$3 8 13 58P \$3 Transmission Lines and Antennas 17 10 27F Control Systems Engineering 35\$5 10 45P \$5 **Embedded Systems** 22 26F 4F VLSI Design and Technology 40 11 51P 15P **Analog Communication** 19 5 24F 10P 260 \$8 Fails 190677 201704576 GAUNS KURTIKAR ASHUTOSH UMESH Digital Signal Processing 36\$4 11 60P \$4 Transmission Lines and Antennas 10 11 21F Control Systems Engineering 24 11 35F **Embedded Systems** 29 6 35F 5F VLSI Design and Technology 24 11 14P 35F **Analog Communication** 21 5 26F 10P 241 \$4 Fails

## **ENGINEERING EXAMINATION SYSTEM**

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COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Bachelor of Engineering (Electronics & Telecommunication)

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### **ENGINEERING EXAMINATION SYSTEM**

COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Page : 5 Of 10

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	VLSI Desigr Analog Com		-		***************************************		**************************************	47	13		81P	21P			
and the state of the specific of the state o	Arialog Corr	irruriication	www.energener		nodes the emproyed processes puts		***************************************	+ / 	13	The production of the first of	60P		15P	404	recommendation of the second second section of
	and an appropriate the second	any filter out out to find out to the first of the first out of the first		nes grenouter materia		-		No the here to object to a constant		a ang it ang 4 At ang ang a foreign ang 4 a sa aban a tang t ang tra Massachan Espherical	and it is detromed in the speciments between the side of the contracting and promoting and the contractions of the contraction of the contractions of the contraction of the contra	andigan, complete of a school with a Managarity of a mile of a facility of the contract of a facility of a sign	month by the second construction of the second seco	481	Passes
90690	201711500	1	Α		M	KUVELKAR	24.54.0000.00000.0000.0000.0000.0000.000		AIBHAV R						
	Digital Signa	erterminen un fein einstelnen der erterlichte der feinberecht der der eine eine der eine der eine der eine der	of months of turning property			en a fair de la contraction de		42	11	10	63P				
	Transmissio	*********				and the state of t	2 - 12 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	40	13	and the state of t	53P				
	Control Syst		eering	eli eli eli este este eli est				46	18		64P				
	Embedded S		mining to the same of the same		error de minimum de majo de servicio de ser	\$P\$\$\\$0\$\$\\$0\$	4140404100144004400044400440044	50	8		58P	6F			
	VLSI Design		the standard and take the Profession Andre	menghabang dan pangka dan s	H. P. R. T. L. S. L.	etraset terrora, anomo terroeta cromo derroter mano que como e		54	16		70P	17P			
ton in the ordinal part of the condition to the ordinal to the ord	Analog Com	munication	and the property contact according	******************************	to the destruction of the section of the			35\$5	9		44P \$6		10P		
	All areas										,			385 \$6	Fails
90691	201704572	1	Α		F	LAWANDE F	RAJVI PR	ASHAN		the Children Charles of Mary and Service and Architecture of Architecture of Service of			comments comments of the special department	and the second section of the second section is a second s	Constitution of the
	Digital Signa	al Processin	g		and the state of t	endus orangent executors research es decide es decide es successes		59	21	23	103P				
AND THE CONTROL OF THE THE PERSON SHOWN	Transmissio		and the second second second second	nnas		ribumanoskuma ajampa aurijamaska maunijanijask hitauri ya ajampanipida a aneg		70	24	The first of the transmission of the second state of the second st	94P				
tra di tra di la comi di tradit	Control Syst	**********************	-	***************				53	23		76P				
	Embedded S	THE STATE OF THE S	Leekt obsportmaniaga Lubus	6014/19060/00000-01400	toreserve energy co		***************************************	88	20		108P	21P	- \$		***************************************
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	9														

# **ENGINEERING EXAMINATION SYSTEM**

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COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Bachelor of Engineering (Electronics & Telecommunication)

SEAT NO	P.R. NO.	ATTEMPTS	ARE	PRO	SEY	NAME OF THE		THEORY	SESSIONAL	TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	1
SEAT NO	P.R. NO.	ATTEMPTS	CD	SEM-	SEA	CANDIDATE	MAX . MIN .	100 40	25	25 Z	125 OR 150 50 OR 60	50 OR 25 25 OR 10	50 OR 25 25 OR 10	850	REMARK
90692	201705121	1	Α		F	MASCAREN	HAS CA	ALIDA	rgheastaranalahidaabkastataranahassabbanah						
	Digital Signa	al Processir	ng					77	19	23	119P				
	Transmissio	n Lines and	d Ante	nnas				64	22		86P				
	Control Syst	ems Engin	eering					72	23		95P				
to the continuence of the first of the second continuence of the conti	Embedded S	Systems						89	23	AND TOTAL TOTAL COST AND	112P	25P			
	VLSI Design	and Techr	nology					88	25		113P	25P			
	Analog Com							66	19		85P	adad a mart vallation, in vincedord valler additivation and value in our meta-device obstance	24P		
														684	Passes
90693	201704594	1	Α	od complete to the process in the second	M	NAGESHKA	R SOHA	AN SHAIL	ENDRA						
	Digital Signa	al Processir	ng					60	20	22	102P				
	Transmissio	n Lines and	d Ante	nnas				51	16		67P				
	Control Syst	ems Engin	eering					57	22	and the second s	79P				
	Embedded 8	Systems						67	18		85P	25P			
	VLSI Design	and Techr	nology	infraging gird in the Previous Start of the				84	21	***************************************	105P	24P		A finaligue from a finalmente des Productions en estimate entre final e en	
	Analog Com	munication	)	en deuts (Mariente de la contraction de				62	19		81P		22P		Juny 11, 400-400 (400 to 110 t
	,													590	Passes
190694	201704599	1	А	TOTAL PROPERTY OF THE PROPERTY	М	NAIK ABHIS	HEK LA	XMIKAN							
	Digital Signa	al Processir	ng				1.46.95824F016.99.49	67	15	18	100P				
	Transmissio	n Lines and	d Ante	nnas				61	16		77P				
	Control Syst	tems Engin	eering					55	19		74P				
	Embedded S	Systems	Acordo I Williams a Control School					52	14		66P	16P			
er val e Esque en emisser en eje es dant i vedirebiskum de en	VLSI Design	and Techi	nology			tita, i qualiti o sevalario i si cicolteri si metales qui ci mittori a lameca si ambie	nde submidelish sendri dis mue pus rivides	74	17		91P	14P			
	Analog Com		TENY / SPANNER IN AN ANTONORY					50	11		61P	and the first transport to the street of the	16P		
mendikan antar sementi melikuan 18. mang melikum untuk	terioristical contraction contraction in the contraction of the contra						CONTRACTOR OF THE STATE OF THE	andre are a come a series a come	TO A STATE OF THE PARTY OF THE	THE PROPERTY OF THE PROPERTY O	and the second s	AND AND THE PARTY OF THE PARTY		515	Passes
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	Digital Signa	al Processir	na		-L	NA NEGOTIANO SE ESTA I SENSITIVA MESSANTI ESTA ESTA ESTA ESTA ESTA ESTA ESTA ESTA		56	15	18	89P		And the second s		
	Transmissio			nnas		n A. Malara sulphose de la livera de la mensione de la proviencia de departamento de la livera de la A.		62	19		81P				
	Control Syst			damente general ancienta de la				51	23		74P	er englis, manus linas up o capa minaga a manasana amasana amasana amasan			
	Embedded S		loomig	numero primero magninisti di distanti i den il	eller o a villaro anter a provin			69	17	The state of the s	86P	20P			
	VLSI Design		nology	 				68	19		87P	14P	and a registration of the state		
	Analog Com					***************************************		62	13		75P		16P		
	7 maiog com		The second second second							Marga agricoga de copia de copa guestro qui ségrap chango anaptique des e tris autor, é com	A comment of the contract of t	The second secon	The state of the s	542	Passes
190696	201704602	1	Α	**************************************	F	NAIK DIVYA	UTTAN	1					- And Sp. 100-100-100-100-1-1-1-100-1-1-1-1-1-1-1	raki di maya kaci 200 i Afrika suyu katapana	
	Digital Signa	al Processir	na					71	18	20	109P				
	Transmission			nnas	them is about as disease.			62	20		82P	alpending house, where his his statement with its thinks of the statement	material participation of the second section of the section of t		
	Control Sys					er af sægre stjerky) i tjerky førtiger yd trittavet (ætgete trittavet		71	20		91P				
	Embedded :	mentiones a letter of contract of the body of the contract of	icering		Part (No restant of square to		ou extensi priurbenos prienes vis com	94	19		113P	16P		The second of the second of the second secon	The Sangar Marie Control of the Annual States of Control of Contro
	VLSI Design		nology	man sahiyinar Simbar Va sihasiyar adı	ir Louit i rigo aben i r Lind	Marriando y o tanar con a considerada sebapatar de sobo de portante e a cata	a the or the control decided for a confined to the particular of particular confined to design the	81	21	- I Agent, former former for control commer executives	102P	20P			
n en transport en principales (a relação periodo en persona de presenta de la despensión de la despensión de l La composição de la composição de la composição de la composição de la despensión de la composição de la despe	Analog Com		and the state of t	7.1727 Tr. (47.2000 17.2000	Parts - Service Servic		****	60	19	g o dignigar, Angles melay i ingrametri principani Ligitim tabah menin pada ma	79P	20P	17P	unit angla making panjaman ayan yanga panga anganan yanganga	ematerna artificial estre est est est est est est est artificial de l'est
r ett spirite still den stille ett settigende et sekseelik i v. v. e. ett stille ove	Arialog Corr	munication								at in providing the certain certain and in the contemporary certain and the ce	196		in erterell Abras lavest settlementeller seemal en	629	Passes
190697	201705004	and a substitute out to the substitute of the su	Α		М	NAIK PRITE	SH ALIA	AS VAIBL	HAV VIKAS			and my verband private in a major of a state of the state	and the first time to the transfer and grant terms		The second secon
(194 - 1944) - Paris - Marie And And - Strand Andrew & Add Andrew & Ad	Digital Signa	·				The state of the s		52	19	23	94P				conduction of the control of the con
	Transmission		PERSONAL PROPERTY.	nnas			ok v katalanka sasta kenatan sasabal	40	17	early constitution of constitution and c	57P		0) 0) 100 100 100 100 100 100 100 100 10		
	Control Sys					er katilland yn de syntae i de fan de de yn yn den de syntae de fan yn den syntae yn geddeld		54	20	And the supplier of the suppli	74P				
	Embedded	encode and the production of the production and the production	icoming					50	19		69P	16P			
	VLSI Design	White was a second to the place of the second of the	nology	······································			ng yan barbarah ya sa bangsa gangan ka sa	75	19		94P	25P			
	Analog Com		**************			ent quem inpresentant a projectiva poi una esta contrava e introduced que pro-		58	21	to decre the process of a gradual and a second of a gradual order.	79P	Polygon Para Security of Security State Security	18P		
este contrator entre en escentrol especialmente, per unite super en entre entre en entre e	2001	- 1 (***********************************	e territorio de la compositación de la composi	ente de l'adotte y describation de				at the state of the second			Anne Carrier	1	en militaria anno con accessoran	526	Passes
190698	201704598	1	Α		М	NAIR SHRIF	IARI SA	BU			enter ten over til en en eller stelle s	erior for the first and received a contraction of the second contraction of the contracti	The conference and the second section of the second sections of the second sections of the second sections of the second section section sections of the second section section section sections and section s	and Department of the control of the	CMS 2 Reports
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	Transmissio		Trees to receive the species	nnas				57	22	A Marie De Marie De Caralle de Ca	79P				
	Control Syst							65	25		90P				
	Embedded 3		201119	consisted conditions of the constraints				91	21		112P	22P			
	- IIIII DO CO CO							- I			1141	441			
			nology		TO SERVICE SERVICES	to the street from the tribute star starts and by the about the shock of the break and the cate of the starts the	en dij dije dij dija dij amidije da similia i pobjidne	88	19	to describe the development of the control of the c	107P	25P	report designate sources of the States Pedinaters devication	the following the grant and the strategic of rates of referring the test and the strategic of the strategic	
	VLSI Design	and Techi	Personal Services of the Services of				en med verbe gitt gitt git mingen til skungen i tredskyden en filmen ming verbe gitt giv en til skungen i tredskyden	88 71	19.		107P 94P	25P	23P		

## **ENGINEERING EXAMINATION SYSTEM**

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COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Bachelor of Engineering (Electronics & Telecommunication)

SEAT NO	P.R. NO.	ATTEMPTS	ADE	PRO	SEY	NAME OF THE	i	THEORY	SESSIONAL	TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	
EATNO	P.R. NO.	ATTEMPTS	CD	SEM-	SEA	CANDIDATE	MAX.	100	25	25	125 OR 150	50 OR 25	50 OR 25	850	REMARK
	004704500	enterte le remarce que se enque ences podembalme	^	V	D.4	NIAWAK CI III	MIN .	40	FOKINIAND	Λ.N.I	50 OR 60	25 OR 10	25 OR 10	340	
90699	201704596	1	Α		М	NAYAK SHI	NKKE C		adressment of singles with the service of the servi		4050		and designation to indicate the country of the control of the cont		
and other days with a color of the second of the days	Digital Signa		ar period of the entrance endeath of		*********		W44500000000000000000000000000000000000	81	21	23	125P		the dealers for the service of the electric services frances and		
	Transmissio	eventures represent the storage of pieces				***************************************	********************	55	21	***	76P				
in the region of the transport of the second and th	Control Syst	Contribute the first particular and contribute to the contribute of the contribute to the contribute of the contribute to the contribute t	eering					49	23		72P		50 OR 25 850		
	Embedded S	Systems					**************************************	85	18		103P	13P	TO A REAL PROPERTY OF THE PARTY		
	VLSI Design	and Techi	nology					76	21		97P	24P			
	Analog Com	munication	1			arrownia waka inga manaka kina ka		46	15	A CONTROL OF THE CONT	61P		24P		
														595	Passes
90700	201705160	1	Α	n, and an interpretation of the	F	NEHA MUR	ГНҮ		et ann an eile e general ann an eile ann an ann an ann an ann an ann an ann an a	a estamos parquesta sintrodos contrata polonymento de estimos colonidades estimos es estimos de estimos estimo	allung yek getter de geroen allen hynnaster byw. Yeber, yek ar van get ar van get	a mangabagan sembada at insaminya mandipilihan arkani arkani anada danyinika m	nine (Balagania a sa yani dhisisa ningani mpin kadhani a 1. Okanbinisa	enganisan (keranda et allaman yadın aydık adalam	Angel State
	Digital Signa	l Processir	and a second		.L			27	9	13	49F				
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					rised in the countries	er Ambricos y numeros y militações produceros as del del en pieça en que de su del compresión de la compresi	****************	45	16		61P	Ar and a contract the contract and a		The state of the section of the sect	
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	VLSI Design		Comment or contract of the land					41	12		53P	16P	- 4 10g 2 d		
	Analog Com	munication	1					35	8		43F		10P		
														339	Fails
90701	201711487	1	Α		F	OLI SUSHAI	NTI P							gaine, ne ana a qua e gen me a carbe mana e un escriba y me escrib	(Promotor)
14-14-14-14-14-14-14-14-14-14-14-14-14-1	Digital Signa	n. grave damme a fritanska dammer v telegogi i en				tv = 100 ta 100	three contributions are a contri	63	15	17	95P				
	Transmissio		deposit months from Jacquist Andre	nnac	minimum necessary named			61	17		78P		er a referencia esta esta esta esta esta esta esta est		
							eatrings as yet well on brokenings abo	60	24		84P				
and the second of the second o	Control Syst		eering		der was de la company de la co		**************************************	68	10	to the second of	78P	10P		han Marina and and an in the first particular and hand an inch and an inch and	
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	Analog Com	munication	]			despuisitions are trace in proceed to second and construction of the about the second		50	18		68P		18P	Andrews are a series manage	
				•				i)						540	Passes
90702	201704574	1	Α		M	PEDNEKAR	AMBAF	R SANJA					en daggensker gøverte gøverger gjes afterstilter en energe	and register to the control of the second section of the second section of the second	Later to determine
	Digital Signa	al Processi	na			va dis versione production accessive description of		61	11	15	87P				
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	Control Syst							40	20		60P	ST 400 44 100 100 100 100 100 100 100 100			
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	Embedded S	tradition of the Carrier of Carrier and American American	pripario vingra importo i materio, to			**************************************		61	16			and the second of the second o			
	VLSI Design		DESCRIPTION OF THE PARTY OF THE	*****************	(debbersonsengelus)	Availant talkastud seksen inn konsel kraken kostonian dieska desk	***************************************	****	and the transfer of the second		77P	22P	***************************************		
i ludget od plet michadost, dout et octobr kodo design	Analog Com	munication	<b>)</b>				-	50	14		64P		20P		
									*				1	488	Passes
190703	201704570	1	Α		M	PILARNEKA	R HRU	SHIKESH	I DINANATH						
	Digital Signa	al Processii	ng	nan industrial an attornamen	a di et a con es secono con	ercondicer of colorina i continue and advisors i principal pear, at quidapopper,	yoursess or end of 260 cases on the	40	11	14	65P				
ere die zu Sergie (1947), die derbeiter geralte de abschrafte ge-	Transmissio	and the state of t	to an dory cited to hardy contents the	nnas	high-go-ghamas Absorption	and the state of t	-1/07-0-1/4-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1	52	16		68P	nd all de commente de la commente de comme	ante alpundon este familia des est. Pre timo fa de algundo este este este est.		San Hallinger (Annahuller) - San Leithara San Leithara (Annahuller)
	Control Syst		de alternativa per intercer per primario calingate que e					58	15		73P	***********************************	Apply the contract to the second section of the second		
	Embedded S	CONTRACTOR STATE OF THE STATE O	coming			Constitution of the control of the c		42	15		57P	12P		***************************************	
an an in in in the their days in all the facts on the in-theres does	VLSI Design	and the contract of the second section of the section of the second section of the section	nology	and a second property ages			PARENTE PARENTE SALES SEALES	47	10		57P	21P			
			managed to be a proper to the proper to be a second	and the service of th			149-1011 objection 100-1020 and 100 an	55	13	a con provided supplied superspical a representation of the contract of the co		21P	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
	Analog Com	munication	Ton order a bordinary orbitol some				protesses and section and designed to a	55	13		68P		13P	And the second s	
SOUTH MANAGEMENT OF THE SECOND STATE OF THE SE			other processors in such a tra-	o months in a minimum										434	Passes
90704	201704591	1	Α		F	RANE DHA	NSHRE	E SANT	OSH						
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	Control Syst					n sindrindra nascoval se adejir la rydengina in device y ngeoloni-quareu	************************	57	25	and the second of the second s	82P		and the second section of the section	******************************	physical and a second section of the section of the second section of the s
	Embedded S	and the state of the same of the state of th	23.1119					85	20	The state of the s	105P	21P			
i en efectivale de la como de la	VLSI Design		nology		-211-1-1-24-1-1-1-1-1-1-1-1-1-1-1-1-1-1-			88	23		111P	21P	angeliga di stapit de les les democrations distributions em		
	REPORTER PROPERTY OF RESTREE AS A STREET A		benique proprietation of complete a					62	20					Polician in the second control and a second control	
	Analog Com	munication	**********************			0 Tarabatan 18 - 2 Tarabatan 18 Banasa 18 Bana		02	VALUE AND THE SECTION OF THE SECTION		82P		24P	· · · · · · · · · · · · · · · · · · ·	Anna Parket and Control of the Contr
	de trade productiva de la companya del companya de la companya del companya de la	engaretracia constante con constante con	Above on the control of the	and the same and the same and the same	-ganaan-		NATION PROPERTY PROPERTY AND ADDRESS AND A		**************************************					627	Passes
90705	201811778	1	Α		М	RODRIGUE	S NASH	ANGEL	0						Al chicken
	Digital Signa	al Processi	ng		talkos estatuaren y	e commencia socialización de sido e del code do de producto de producto se que de de producto de sido de code co	14 tina jewelat (miasa) (piwelat) wi ka	44	6	15	65P	entile concentration to the property to a rather an after few and	and a spin and an array of the spin and array of the spin array of th		10.100
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### **ENGINEERING EXAMINATION SYSTEM**

Page: 8 Of 10 COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM Bachelor of Engineering (Electronics & Telecommunication) RC 2016-17 College:-Government of Goa College of Engineering **EXAM YEAR:**November 2019 ATTEMPTS ARE PRO SEX NAME OF THE TOTAL 125 OR 150 THEORY SESSIONAL TERM WORK PRACTICAL TOTAL SEAT NO P.R. NO. ORAL 50 OR 25 REMARKS 50 OR 25 SEM-CANDIDATE MAX. 100 850 50 OR 60 25 OR 10 25 OR 10 340 MIN . 40 **ROHAN KUMAR** 190706 201705150 18 99P 67 14 Digital Signal Processing 61 17 78P Transmission Lines and Antennas 56 21 77P Control Systems Engineering 61 76P 25P 15 **Embedded Systems** 80 104P 20P 24 VLSI Design and Technology 68 18 86P 23P **Analog Communication** 588 Passes 190707 201705161 SAHANA P KULKARNI 15 19 102P Digital Signal Processing 70 17 87P Transmission Lines and Antennas 63 86P 23 Control Systems Engineering 73 16 89P 24P **Embedded Systems** 88 23 111P 25P VLSI Design and Technology 62 19 81P 19P **Analog Communication** 624 Passes 190708 201811776 SARAVANNAVAR ASHWINI P 18 19 110P Digital Signal Processing Transmission Lines and Antennas 59 13 72P 50 21 71P Control Systems Engineering 81P **Embedded Systems** 68 13 25P 60 18 VLSI Design and Technology 78P 24P 55 10 65P 21P **Analog Communication** 547 Passes 190709 201704564 Α SAWANT ANIL APPA Digital Signal Processing 28 11 12 51F Transmission Lines and Antennas 35\$5 10 45P \$5 Control Systems Engineering 26 10 36F 30 11 **Embedded Systems** 41F 12P 43 12 55P VLSI Design and Technology 16P 30 6 36F 10P Analog Communication 302 \$5 Fails 190710 201811774 SHARMA UPESH Digital Signal Processing Abs 8 12 20F Transmission Lines and Antennas 8 10 18F Control Systems Engineering 24 10 34F **Embedded Systems** 41 10 51P 10P VLSI Design and Technology 46 12 58P 14P 23 **Analog Communication** 7 30F 10P 245 Fails 190711 201704593 M SHET NARVEKAR VARAD GOKULDAS Digital Signal Processing 40 12 10 62P Transmission Lines and Antennas 57 11 68P Control Systems Engineering 44 15 59P **Embedded Systems** 40 44P \$6 11P VLSI Design and Technology 57 13 70P 18P **Analog Communication** 35\$5 9 44P \$6 12P 388 \$12 Passes 190712 201704603 SHINKRE NEHA JAGANNATH Digital Signal Processing 76 117P Transmission Lines and Antennas 58 22 80P Control Systems Engineering

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**Embedded Systems** 

**Analog Communication** 

VLSI Design and Technology

25

23

24

22

92P

116P

115P

95P

25P

22P

23P

685

Passes

### **ENGINEERING EXAMINATION SYSTEM**

Page : 9 Of 10

COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

**Analog Communication** 

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**Analog Communication** 

# **ENGINEERING EXAMINATION SYSTEM**

COLLEGE WISE STUDENT RESULT REGISTER FOR SEM-V EXAM

Page: 10 Of 10

College :-G	overnment of Go	a College of	Engin	eering									EXAM	YEAR:Nove	C 2016-17 mber 2019
SEAT NO	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM- V	SEX	NAME OF THE CANDIDATE	MAX . MIN .	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 850	REMARKS
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Declared On

Assistant Registrar-E(Prof)

Assistant Registrar Exam

Controller of Examinations

P:Passes; F:Fails; Abs:Absent; N:Non Appearance; D:First Class with Distinction; I: First Class; S:Second Class; NE:Not Eligible; +:Marks carried over Discrepancy If any in the Result shall be informed to the University within a week from the date of receipt of the result by the colleges.





Course: RC 2016-17

RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER NOVEMBER 2019

EXAM

COLLEGE: GOA COLLEGE OF EN	IGINEERING		EXAM					
SEAT No PR No GENDER Attempts								
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Analog Communication	40			51 +		13 +		
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Embedded Systems	64	12		76 +	10 +			
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Course: RC 2016-17

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RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER NOVEMBER 2019

EXAM

PAPER DESCRIPTION

201610637 F

Transmission Lines and Antennas

Digital Signal Processing

Embedded Systems

**Analog Communication** 

Control System Engineering

VLSI Design and Technology

SEAT No PR No GENDER Attempts NAME OF CANDIDATE

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Read By :

Checked By:

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Assistant Registrar-E(Proff.)

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VELIP KALPITA B

Assistant Registrar Exam

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